

Atty. Dkt. No. 039153-0433 (C167596-CIP)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Sander, et al.

Title: MINIMIZING TRANSISTOR
SIZE IN INTEGRATED
CIRCUITS

Appl. No.: 10/042,732

Filing Date: 04/25/2001

Examiner: Nguyen, Thanh

Art Unit: 2813

CERTIFICATE OF FACSIMILE TRANSMISSION	
I hereby certify that this paper is being facsimile transmitted to the United States Patent and Trademark Office, Alexandria, Virginia on the date below.	
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March 3, 2004 (Date of Deposit)	

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OFFICIAL

This communication is responsive to the Non-Final Office Action dated December 3, 2003 concerning the above-referenced patent application.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this document.

Remarks/Arguments begin on page 6 of this document.

Please amend the application as follows:

001.1668427.1

Application Serial No. 10/042,732